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Jacqueline J. Garner

Texas Instruments Incorporated

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IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant:

Chambers

Art Unit:

2829

Serial No.:

10/649,425

Examiner:

Sarkar, A.

Filing Date:

08/27/2003

Docket No.:

TI-35714

Customer No.: 23494

Conf. No.:

5169

Title: STRUCTURE AND METHOD TO FABRICATE SELF-ALIGNED TRANSISTORS

WITH DUAL WORK FUNCTION METAL GATE ELECTRODES

CERTIFICATION OF FACSIMILE TRANSMISSION

I hereby certify that the following papers are being transmitted by facsimile to the U.S. Patent and Trademark Office at 703-872-9306 on the date shown below:

Jacqueline d. Gamer, Reg. No. 36,144

April 20, 2005

Date

FACSIMILE COVER SHEET

X FACSIMILE COVER SHEET NEW APPLICATION DECLARATION (# Pages) ASSIGNMENT (# Pages) FORMAL DRAWINGS INFORMAL DRAWINGS CONTINUATION APP'N (# Pages) DIVISIONAL APP'N		X AMENDMENT (10 Pages) X EOT 3mth (2 Pages) NOTICE OF APPEAL (# Pages) APPEAL (# Pages) ISSUE FEE (# Pages) REPLY BRIEF (IN TRIPLICATE) (# Pages)
NAME OF INVENTOR(S):		RECEIPT DATE & SERIAL NO.:
Chambers		Serial No.: 10/649,425 Filing Date: 08/27/2003 Conf. No.: 5169
TITLE OF INVENTION:		
STRUCTURE AND METHOD		
TI FILE NO.:	DEPOSIT ACCT. NO.:]
TI-35714	20-0668	
FAXED: 4/20/2005 DUE: 1/20/2005 ATTY/SEC'Y: JJG		

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Texas Instruments Incorporated PO Box 655474, M/S 3999 Dallas, TX 75074

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AMENDMENT UNDER 37 CFR 1.111

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Jacqueline, J. Garner Reg. No. 36,144

Commissioner for Patents P.O. Box 1450 Alexandria, VA 22313-1450

Dear Sir:

The following amendments and remarks are offered in response to the Examiner's Office Action dated 10/20/2004. They are respectfully submitted as a full and complete response to that Action.

Please amend the above-referenced application as follows:

There are no Amendments to the Specification.

Amendments to the Claims are reflected in the listing of claims that begin on page 2 of this paper.

There are no Amendments to the Drawings.

The Remarks begin on page 7.